

PATENT APPLICATION

Attorney Docket No.: 01-384

Date: September 21, 2001

ATTN: BOX PATENT APPLICATION Commissioner for Patents Washington, D.C. 20231

NEW APPLICATION TRANSMITTAL

Transmitted herewith for filing is the patent application of

Inventor(s): Newell E. Chiesl

For (title): Arrangement for Measuring Pressure on a Semiconductor Wafer and an

Associated Method for Fabricating a Semiconductor Wafer

Enclosed are:

[x] Patent Application Data Sheet

[x] 15 sheet(s) of drawings (2 sets) (INFORMAL)

An executed Assignment of the invention to LSI Logic Corporation

[x] An executed Declaration and Power of Attorney is enclosed

[] Material information pursuant to 37 CFR §1.56.

[] It is expressly requested that the U.S. Patent and Trademark Office commence national processing of the above-entitled international application under the provisions of PCT Article 23(2) and 35 USC 371(f).

[x] Nonpublication Request Under 35 U.S.C. 122(b) (2) (B) (i)

[x] Return Postcards(2)

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The filing fee has been calculated as shown below:

CLAIMS AS FILED

FOR	NUMBER FILED	NUMBER EXTRA	RATE	FEE
TOTAL CLAIMS	20 - 20 =	0	x \$18	00.00
INDEPENDENT CLAIMS	3 - 3 =	0	× \$80	00.00
		BASIC FEE		\$710.00
		FI	LING FEE	\$710.00

[x] The Commissioner is hereby authorized to charge the basic filing fee (\$710.00) to Deposit Account No. 12-2252. Two copies of this sheet are enclosed.

[x] The Commissioner is hereby authorized to charge any fees under 37 CFR §1.16 and 1.17 which may be required during the pendency of the application to Deposit Account No.12-2252.

"Express mail" mailing label number <u>EL904368373US</u> Date of Deposit <u>September 21, 2001</u> I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR §1.10 on the date indicated above and is addressed to the Commissioner of Patents, Box PATENT APPLICATION, Washington, D.C. 20231.

Bradford G. Addison
(Name of person mailing application)

(Signature of person mailing application)

Newell E. Chiesl

Arrangement for Measuring Presu on a Semiconductore Wafer and a Associated Method for Fabricati Presur and ar q a Semiconductor Wafer Atty Docket Number

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I hereby certify that the invention disclosed in the attached application has not and will not be the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing.

I hereby request that the attached application not be published under 35 U.S.C. 122(b).

September 21, 2001

Signature

Bradford G. Addison

Typed or printed name

This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application upon filing.

Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.

If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant must notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application. Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).